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AUSTIN, TEXAS 78731***FACSIMILE TRANSMITTAL TO USPTO***

To: USPTO Central	Fax No.: (571) 273-8300
Date: March 8, 2006	Pages: 16 (including this sheet)
From: Andrew C. Graham	Sender's Phone No.: (512) 338-6300 Sender's Fax No.: (512) 338-6301

REGARDING:

Title:	MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND LOGIC BLOCK STRUCTURE AND METHOD THEREFOR		
Application No.: 10/675,212	Filed:	September 30, 2003	
Examiner: Ho, Hoai V.	Group Art Unit:	2827	
Atty. Docket No.: 023-0024	Confirmation No.:	9946	

ATTACHED HERETO:

- (1) Notice of Appeal from the Examiner to the Board of Patent Appeals and Interferences (1 page)
- (2) Response After Final Rejection (37 C.F.R. § 1.116) (12 pages)
- (3) Transmittal Letter (2 pages)

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Andrew C. Graham3-8-06
Date

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March 8, 2006

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant: Roy E. Scheuerlein, et al.
Title: MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND LOGIC
BLOCK STRUCTURE AND METHOD THEREFOR
Application No.: 10/675,212 Filed: September 30, 2003
Examiner: Ho, Hoai V. Group Art Unit: 2827
Atty. Docket No.: 023-0024 Conf. No.: 9946

Dear Sir:

Transmitted herewith are the following document(s) in the above-identified application:

- (1) This Transmittal Letter (2 page(s))
- (2) Notice of Appeal from the Examiner to the Board of Patent Appeals and Interferences (1 page(s))
- (3) Response After Final Rejection (37 C.F.R. § 1.116) (12 pages)

Fees:

Notice of Appeal	\$500.00
n/a	\$0.00
TOTAL FEE:	\$ 500.00

- ☐ A check is enclosed for the Total Fee shown above.
- ☒ Please charge the Total Fee shown above to Deposit Account 50-0631.
- ☐ Please charge the Total Fee shown above to credit card (Form PTO-2038 enclosed).
- ☒ The Commissioner is hereby authorized to charge any deficiency in fees and any additional fees under 37 C.F.R. § 1.16 or 1.17 that may be required during the pendency of this application or by the papers submitted herewith, and to similarly credit any overpayment, to Deposit Account 50-0631.

PTO Transmittal 060308.doc

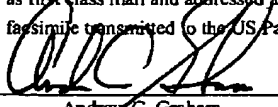
ZAGORIN O'BRIEN GRAHAM LLP

Commissioner for Patents

March 8, 2006

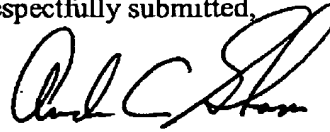
RE: 10/675,212

Page 2 of 2

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 Andrew C. Graham	<u>3-8-06</u> Date

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Respectfully submitted,



Andrew C. Graham, Reg. No. 36,531

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PATENT

MAR 09 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein, et al.

Title: MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND
LOGIC BLOCK STRUCTURE AND METHOD THEREFOR

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Confirmation No.:

9946

March 8, 2006

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE AFTER FINAL REJECTION (37 C.F.R. § 1.116)**

This paper is being submitted following the Final Rejection mailed on December 8, 2005.
In light of the Amendments and/or Remarks herein, further consideration is requested.

Amendments to the Claims begin on page 2.

Remarks begin on page 10.